



EXPEDITED PROCEDURE – EXAMINING GROUP 2816

S/N 10/731,393

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant: | Chaiyuth Chansungsan et al. | Examiner: | Tuan T. Lam |
| Serial No.: | 10/731,393 | Group Art Unit: | 2816 |
| Filed: | December 9, 2003 | Docket No.: | 884.A83US1 |
| Title: | POWER SUPPLIES NOISE DETECTOR FOR INTEGRATED CIRCUITS | | |
| Assignee: | Intel Corporation | Customer Number: | 21186 |

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed April 20, 2006, please amend the application as follows:

[Handwritten signature]

Do not
enter
T. Lam
6/30/06